

DOCKET NO. 00-BN-067 (STMI01-00067)

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PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Intellectual application of : Anthony X. Jarvis, et al.

Serial No. : 09/751,679

Filed : December 29, 2000

For : INSTRUCTION FETCH APPARATUS FOR WIDE ISSUE
PROCESSORS AND METHOD OF OPERATION

Group No. : 2183

Examiner : (Not Yet Known)

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

CERTIFICATE OF MAILING

I hereby certify that this correspondence, including the attachments listed, is being deposited with the United States Postal Service, via First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231 on 5/3/01 (Date)

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